

10091489-052201

501.41263X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

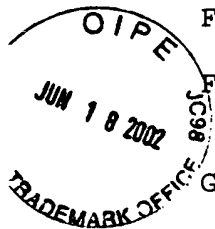
Applicant(s): NAKABAYASHI, et al

Serial No.: 10/091,489

Filed: March 7, 2002

For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE

Group: 2812



PETITION FOR EXTENSION OF TIME

Commissioner for Patents  
Washington, D.C. 20231

June 18, 2002

Sir:

In the matter of the above-identified application, applicant (s) hereby respectfully petition for an extension of time to permit filing of a response within the one month subsequent to the shortened statutory period set in the Notice to File Missing Parts of Nonprovisional Application mailed April 8, 2002. A payment in the amount of \$110.00 to cover the required fee for the requested extension of time is attached hereto.

It is respectfully requested that any shortage in the fee be charged to the to Deposit Account No. 01-2135 (501.41263X00) and please credit any excess fees to such deposit account.

Respectfully submitted,

William I. Solomon  
Registration No. 28,565  
ANTONELLI, TERRY, STOUT & KRAUS, LLP

TELEPHONE 00000007 10091489

TO FEE

110.00 BP

WIS/gfa  
Attachment(s)  
(703) 312-6600